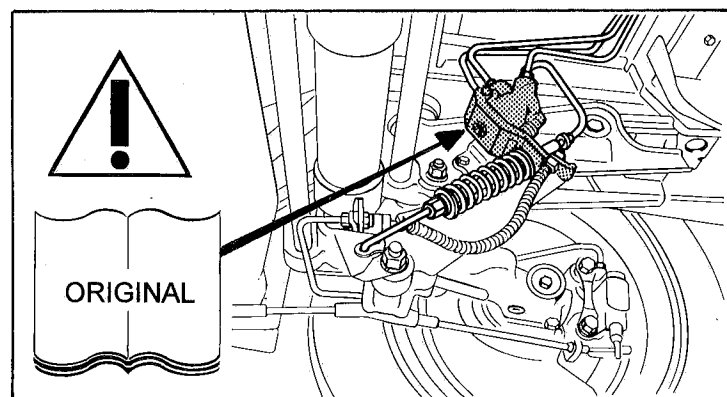
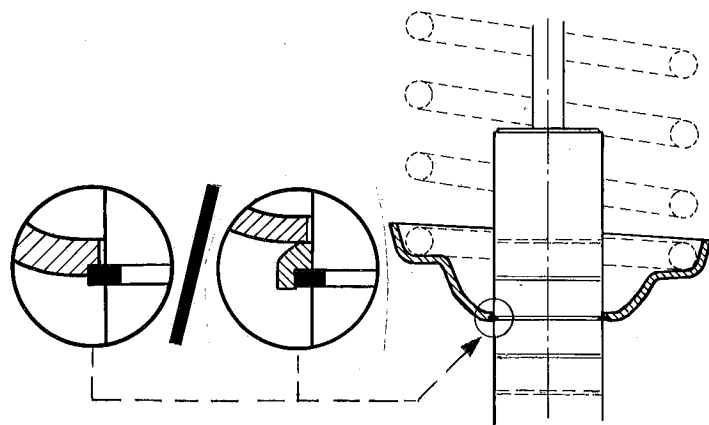
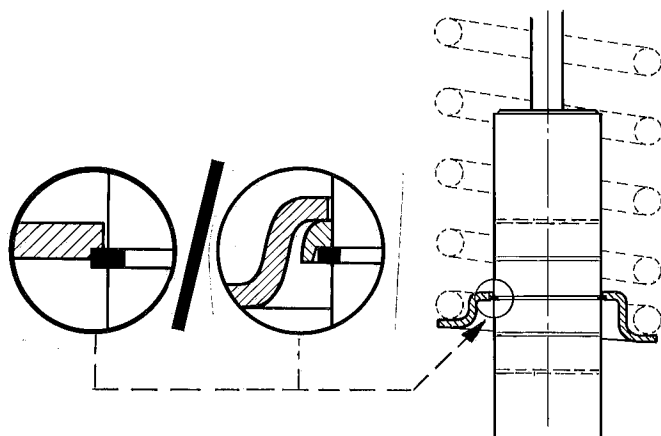
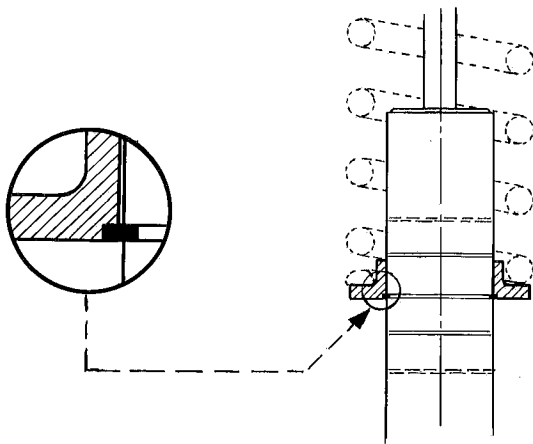
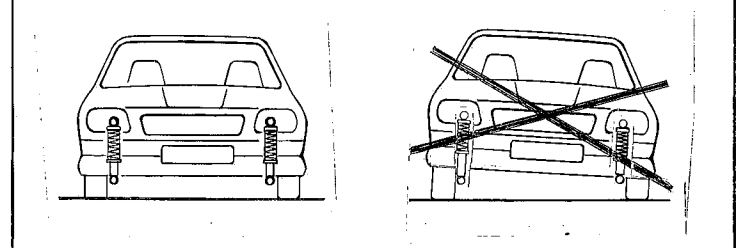
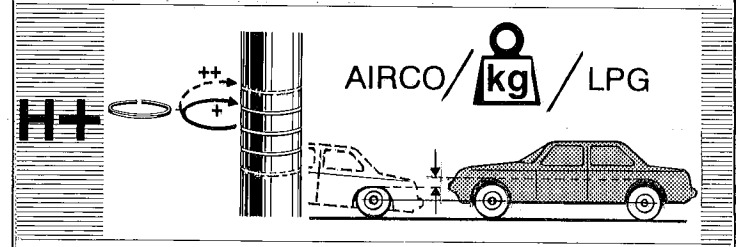
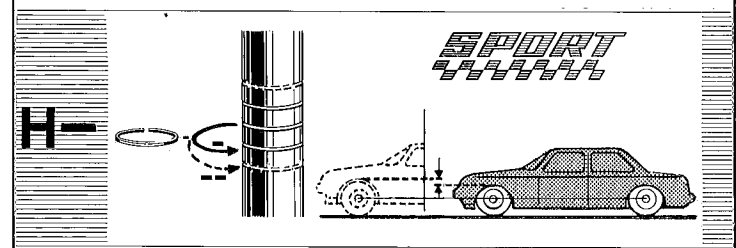
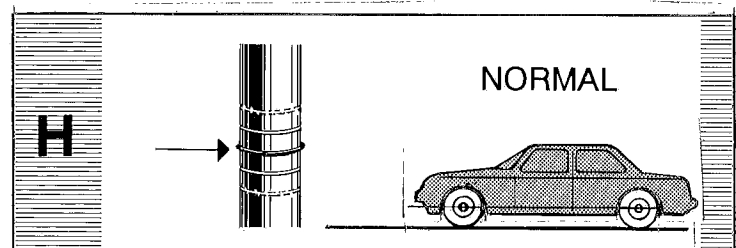
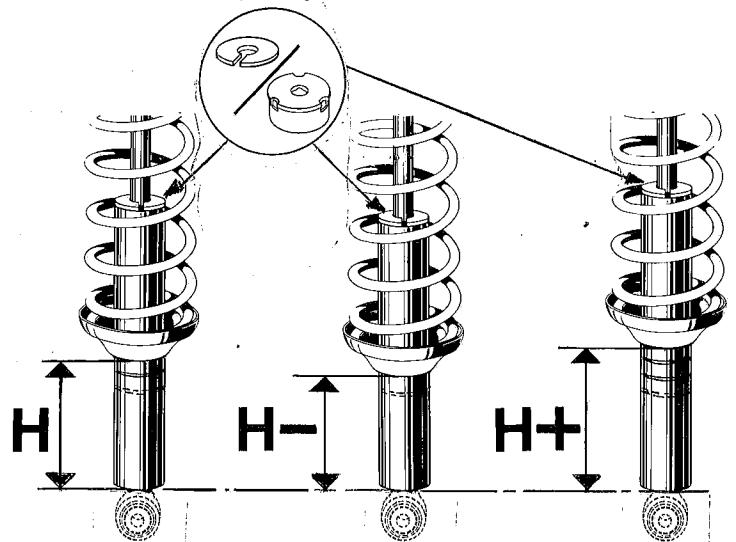
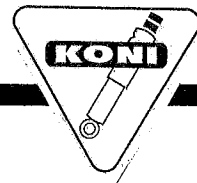
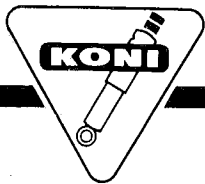


instruction

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107 /2

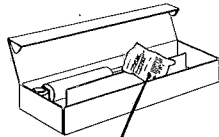
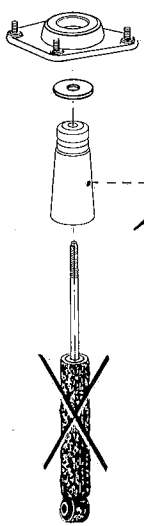




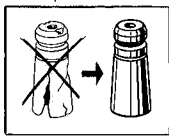
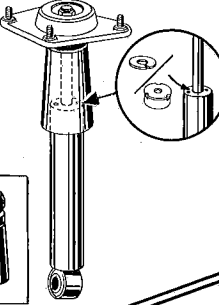
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301/2

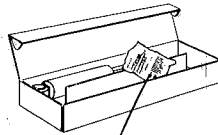
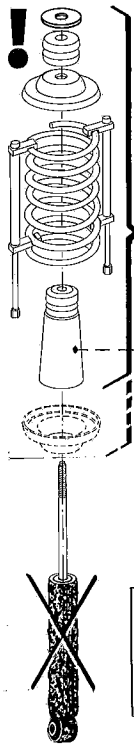
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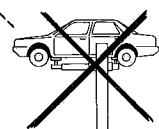
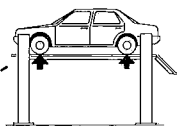
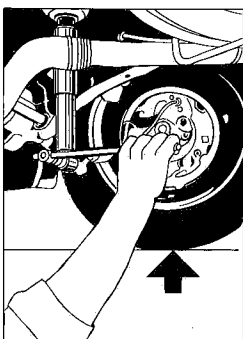
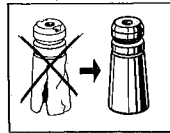
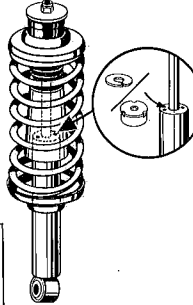
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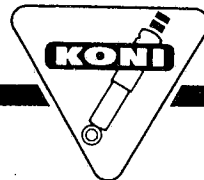
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NEW



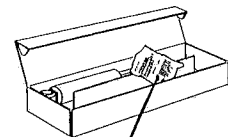
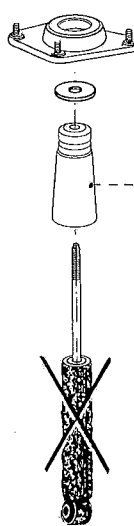
AFST. / 1A →



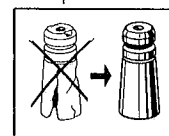
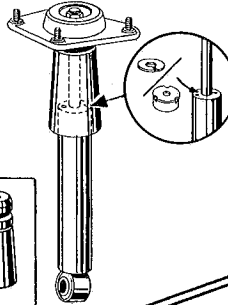
instruction

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301/2

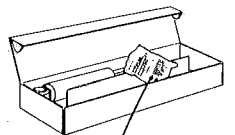
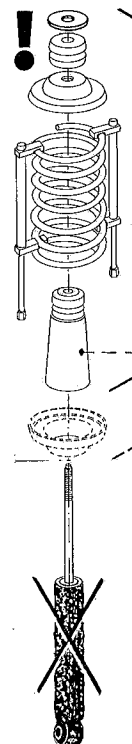
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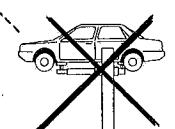
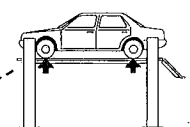
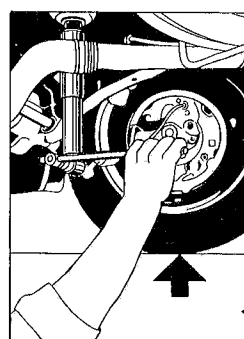
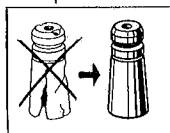
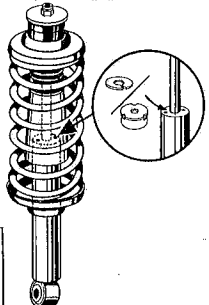
NEW



ORIGINAL



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